RFM69 on ATmega

Now that we have the RFM69 working on Raspberry Pi and Odroid C1, we’ve got all the pieces to create a Wireless Sensor Network for home monitoring, automation, IoT, etc.

But I absolutely don’t want to leave the current range of JeeNodes behind. Moving to newer hardware should not be about making existing hardware obsolete, in my book!

The JeeNode v6 with its on-board RFM12 wireless radio module, Arduino and IDE compatibility, JeePorts, and ultra-low power consumption has been serving me well for many years, and continues to do so — with some two dozen nodes installed here at JeeLabs, each monitoring power consumption, house temperatures, room occupancy, and more. It has spawned numerous other products and DIY installations, and the open-source jeeLib library code has opened up the world of low-cost wireless signalling for many years. There are many thousands of JeeNodes out there by now.

There’s no point breaking what works. The world wastes enough technology as it is.

Which is why, long ago a special RF69-based “compatibility mode” driver was added to JeeLib, allowing the newer RFM69 modules to interoperate with the older RFM12B’s. All you have to do, is to add the following line of code to your Arduino Sketches:

#define RF69_COMPAT 1

… and the RFM69 will automatically behave like a (less featureful) RFM12.

This week is about doing the same, but in reverse: adapting JeeLib’s existing RF12 driver, which uses a specific packet format, to make an RFM12 work as if it were an RFM69:

- Classic vs native packets – Wed
- RF compatibility options – Thu
- RF69 native on ATmega’s – Fri
- Using RFM12’s with RFM69 native – Sat

As I’ve said, I really don’t like to break what works well. These articles will show you that there is no need. You can continue to use the RFM12 modules, and you can mix them with RFM69 modules. You can continue to use and add Arduino-compatible JeeNodes, etc. in your setup, without limiting your options to explore some of the new ARM-based designs.

Let me be clear: there are incompatibilities, and they do matter at times. Some flashy new features will not be available on older hardware. I don’t plan to implement everything on every combination, in fact I’ve been focusing more and more on ARM µC’s with RFM69 wireless, and will most likely continue to do so, simply to manage my limited time.

Long live forward compatibility, i.e. letting old hardware inter-operate with the new…
Classic vs native packets

There are so many different wireless radio modules and different packet protocols, that it’s easy to get lost. In the context of JeeLabs, the radio modules currently in use are the RFM12 and RFM69, both produced by HopeRF.

At some point, a rumour started echoing on internet that the RFM12 was being abandoned and replaced by the RFM69, but after over a year it is now clear that both will continue to be produced for a long time to come – if only because there is plenty of demand.

The use cases for both types are quite similar, i.e. being able to send and receive short packets on the 433, 868, or 915 MHz bands, but there are also some major differences:

- the RFM12 module can only store 1..2 bytes in its FIFO, meaning that data must be read and written at a fairly high rate (within 200 μs with the default JeeLib settings)
- the RFM69 can store an entire packet, so there are no hard real-time timing requirements, other than to clear the FIFO for the next reception or transmission
- the RFM69 is more sophisticated, in that many settings are more detailed – and it supports more modulation-, synchronisation-, and packet-formatting modes
- the RFM69 supports hardware encryption, based on 128-bit AES, and data whitening, which helps better recover the data rate clock in some edge cases
- the RFM69’s receiver is more sensitive, and its transmitter can be more powerful, leading to a potentially much larger range with the same current consumption

All in all, the RFM69 can indeed be considered a next-generation design – although the RFM12 continues to be just fine for lots of home-monitoring and -automation scenarios. But there is one tricky difference: the on-air packet format / protocol of the RF12 driver (implemented by software in JeeLib) is not the same as the format in the RFM69 hardware-based packet engine:

The “CLASSIC” packet layout used by the RF12 driver is:

- preamble, SYNC bytes, header byte, packet length, payload, CRC bytes

The “NATIVE” packet layout used by the RFM69 hardware is:

- preamble, SYNC bytes, packet length, payload, CRC bytes

We can easily designate the first byte of the payload to act as header byte, but we cannot change the order of the length and header bytes. Furthermore, the length differs: on the RFM12, it does not include the header byte, whereas on the RFM69 it does.

There are other differences in specific bits and in how the CRC is calculated, but the main difference preventing trivial inter-operability is really that header-length-byte order.

Obviously, senders and receivers need to agree on the protocol used.

So far, the code in JeeLib for both RFM12 and RFM69 has been aimed at implementing the classic packet format everywhere. By adding the following define into our code:

```
#define RF69_COMPAT 1
```

… we have the option to use an RFM69 module, while keeping the classic packet layout. This effectively makes the RFM69 backwards compatible: it acts like an older RFM12.

But there is a price to pay, because we can’t use some of the newer features of the RFM69 this way, in particular full-packet buffering and hardware-based AES encryption.

On the ARM platform, we’ve been using a new native “RF69” driver (in rf69.h) which is considerably simpler (less code), doesn’t need fast interrupt support, and offers optional encryption. But while doing so, we’ve also introduced a serious incompatibility.

Now seems like a good time to re-investigate all the different options and combinations…

[Back to article index]
RF compatibility options

Let’s define the playing field first, and all the associated terminology:

- **Classic** packets are the ones understood by the RF12 driver in JeeLib
- **Native** packets are built into RFM69 hardware and the RFU driver in Embello
- **RFM12** is a HopeRF wireless module, with limited FIFO & logic (one B variant)
- **RFM69** is the newer HopeRF module (there are W, CW, and HW variants)
- **RF12** is the driver to JeeLib for Arduino IDE use, supporting classic packets
- **RF69** is a slightly different API supporting the RFM69 in native mode
- **Arduino** is the name of the IDE used to build/upload JeeLib-based code
- **Linux** is the Unix-style mechanism to build/upload Embello-based code
- **ARM** is the architecture of ATMega and ATtiny chips made by Atmel
- **ARM** is the architecture of NXP’s LPC11xx series and many other vendors

And to elaborate a bit more on this:

- **Classic** vs Native refers to the packet format traveling through the air
- **RFM12** vs **RFM69** refers to different wireless radio hardware modules
- **RF12** vs **RF69** refers to different software and their slightly different APIs
- **Arduino** vs **Make** refers to the cross-build + upload environments
- **AVR** vs **ARM** refers to the different p4, architectures (and R- vs 32-bit)

That’s five pairs of variations, for a theoretical 32 different build and usage combinations! Not all of them make sense, and fewer still need to be implemented for interoperability.

The most widely used setup is currently (because it was the first available on JeeNodes):

- **Classic** + **RFM12** + **RF12** = **Arduino** + **AVR**

While the most recent developments at JeeLabs have focused on this configuration:

- **Native** + **RFM69** + **RF69** + **Make** = **ARM**

In other words: it couldn’t be more different and these two cannot inter-operate as is.

**Using RF69_COMPAT**

This configuration uses “RF69_compat” and can be characterised as:

- **Classic** + **RFM69** + **RF12** = **Arduino** + **AVR**

It’s what allows an RFM69-based node to play nice in an RFM12/RF12-type network.

**On Raspberry Pi**

Recent developments have made it possible to introduce a third platform, i.e. Linux running on Raspberry Pi’s (any model) or the mostly-header-compatible Odroid C1.

Though omitted from the above list of terms to avoid confusion, it could be described as:

- **Native** + **RFM69** + **RF69** = **Linux**-make + **RasPi**

This implementation is designed to be used with the RasPi RF board.

**Long term trend**

Given some of the new features offered by the RFM69 in native mode, it’s safe to assume that in the long run, the following configurations will become mainstream at JeeLabs:

- **Native** + **RFM69** + **RF69** + some-build-system + some-architecture

What this means, is that “on the air”, the NATIVE packet format will at some point start to dominate. And while we could easily set our environment to support native and classic packets alongside each other – using a different frequency or net group or both, with two central nodes running in parallel – this wouldn’t be the most convenient setup, long term.

A much better strategy will be to just bite the bullet and make everything work in native packet format. Then we could mix and match, old and new, RFM12’s and RFM69’s, and still be able to treat the entire wireless network as a single one.

This is precisely what the upcoming two articles intend to describe.

**Using RFM69 with Arduino**

Luckily, the new RF69 driver is extremely portable. It started out on LPC11xx ARM chips, but has been extended to run under Linux as well – using a simple “RasPi RF” board. The next article in this series will show that it can also be used in the Arduino environment:

- **Native** + **RFM69** + **RF69** + **Arduino** + **AVR**

A new “RF12_COMPAT” option

But the most interesting option perhaps, is to bring the RFM12 into the future with a modification to the RF12 driver to make it work with native packets (keeping its API).

- **Native** + **RFM12** + **RF12** + **Arduino** + **AVR**

This could in fact be called “Forward compatibility”: if RFM12 can be made to act as if it were a more recently produced RFM69. Because of this, an installed base of RFM12’s need not prevent progress and the use of the more advanced RFM69’s in the rest of the network.

There are trade-offs, of course. Perhaps the most important one is that the RFM69’s AES encryption engine is not available on the RFM12. But even this is not a hard restriction: in principle, AES could be implemented in software in a future extension of the RF12 driver.

Stay tuned for these two exciting new options…

[Back to article index]
The RF69 driver on Github is highly portable, due to the use of C++ templates to keep all platform-specific details strictly separate. All we need to use this driver in the Arduino IDE is to include JeeNode.h, to implement an SPI class with the proper API (see [29]).

template<typename T>
class SpiDevice {
  static uint8_t spiTransferByte(uint8_t out) {
    while ((SPIR & (1<<SPIF)) == 0) return SPBR;  
  }

public:
  static void write [const div](digitalWriteN, int);  
  pinModeN, OUTPUT;    
  pinModeN[1], OUTPUT);  
  pinModeN[2], INPUT;  
  pinModeN[3], OUTPUT);  
  SPI = SPI_H [1] (SPI锃);  
  SPI = SPI_L [1] (SPIprowadzi);  
  static uint8_t read [const div](uint8_t cmd, uint8_t in) {
    digitalWriteN, 0;  
    spiTransferByte(cmd);  
    uint8_t in = spiTransferByte(val);  
    digitalWriteN, 1;  
    return in;  
  }
  
  typedef SpiDevice<> SpiDevice;

The template argument "T" is the pin we are going to use as master SPI select, i.e. 10.

Note that there is also an "SPI" class defined in the newer Arduino IDE releases, which you could use as basis for this SpiDevice definition. It would make things more compatible if you need to use multiple SPI devices – but the above definition will be fine for simple uses.

Using the new driver takes a little setup, since the Arduino IDE expects files to be in a specific place and the sketch directory to have a specific name, whereas this driver lives in the sample directory, which has a different layout (and is more general than the IDE). See the repository, which has a different layout (and is more general than the IDE). See the repository, which has a different layout (and is more general than the IDE). See the repository, which has a different layout (and is more general than the IDE). See the repository, which has a different layout (and is more general than the IDE).

The RF69 driver on Github for how to set things up in your Arduino IDE environment.

The RF69 native on ATmega's is highly portable, due to the use of C++ templates to keep all platform-specific details strictly separate. All we need to use this driver in the Arduino IDE is to include JeeNode.h, to implement an SPI class with the proper API (see [29]).

Verification again, call "rf.encrypt("mysecret");" Encryption is easy with the new RF69 driver, just define a 1..16-char encryption key:

As initialization takes a node ID (RF69 supports 1..60), group, and frequency you need to declare an RF69 instance to use this driver.

Saving to an external line is essentially the same as with the RF12 driver:

The return value is the actual packet size, which may exceed the data stored in rxBuf if that sending is essentially the same as with the RF12 driver

The numbers in parentheses are OK 80180D0102030405060708090A0B0C (130+20:4)

OK 8018090102030405060708 (130+6:4)

OK 80180801020304050607 (130+38:3)

The RF69 native on ATmega's is highly portable, due to the use of C++ templates to keep all platform-specific details strictly separate. All we need to use this driver in the Arduino IDE is to include JeeNode.h, to implement an SPI class with the proper API (see [29]).

The RF69 native on ATmega's is highly portable, due to the use of C++ templates to keep all platform-specific details strictly separate. All we need to use this driver in the Arduino IDE is to include JeeNode.h, to implement an SPI class with the proper API (see [29]).

The RF69 native on ATmega's is highly portable, due to the use of C++ templates to keep all platform-specific details strictly separate. All we need to use this driver in the Arduino IDE is to include JeeNode.h, to implement an SPI class with the proper API (see [29]).

The RF69 native on ATmega's is highly portable, due to the use of C++ templates to keep all platform-specific details strictly separate. All we need to use this driver in the Arduino IDE is to include JeeNode.h, to implement an SPI class with the proper API (see [29]).

The RF69 native on ATmega's is highly portable, due to the use of C++ templates to keep all platform-specific details strictly separate. All we need to use this driver in the Arduino IDE is to include JeeNode.h, to implement an SPI class with the proper API (see [29]).
Using RFM12’s with RFM69 native

So far so good — we now have the RFM12 running in native packet mode using the RF69 driver, for LPC81X ARM μC’s, ATmega328 SeeNodes, and Raspberry Pi’s w/ RasPiRF.

But let’s not leave the RFM12 modules behind, and the many Arduino IDE projects using the RF12 driver in JeeLib. If only it could send and receive native RFM69-type packets!

Well, it turns out that the RFM128 wireless module can be tricked into doing just this. There are several issues involved:

- the frequency, FSK swing, and bandwidths can be chosen to match the RFM69
- likewise, the preamble and sync bytes can be chosen to work with both modules
- the RFM69’s CRC can be computed in software (it’s not the same as in RF12)
- the data whitening used in the RFM69 can also be emulated in software
- and lastly, differences in packet / header layouts can all be handled in software

Quite a few differences, but by carefully choosing the parameters on both types of modules, we can indeed get packets across in both directions. An updated rf12_compat driver has been committed to GitHub with all the necessary changes.

To enable this “native + RFM12 + RF12 = Arduino + AVR” mode, change one define in rf12.h to set RF12_COMPAT to 1 (not to be confused with RF69_COMPAT mode!):

#define RF12_COMPAT 1

Note that this change needs to be made in JeeLib itself; you cannot simply add such a define to your own sketch. The reason for this is that the changes are much more pervasive.

The result is an “RF12 driver” with a virtually unmodified “RF12 API”, i.e. you can poll using rf12_recvDone(), etc – just as you would with a classic setup.

But there are some important differences when using RF12_COMPAT mode:

- the packet layout is different (rf12_len and rf12_hdr are defined differently)
- there’s a new rf12_dst field, the lower 6 bits contain the destination node ID (or 0)
- the lower 6 bits of rf12_hdr always contain the node ID of the sending node
- RF12_HDR_ACK is defined as bit 0 (this is bit 5 in classic mode packets)
- RF12_HDR_DST is no longer present, since there is now a rf12_dst field

The RF12_ACK_REPLY test is not working right now, due to the above flag bit changes.

For an example, see the new rf12_compat.ino sketch:

```java
#include <JeeLib.h>

millisTimer_t timer; // Throughout this sketch.

void setup() {
  Serial.begin(57600);
  Serial.println("[rf12compat]");
  rf12_init(63, RF12_868MHZ, 42, 1720); // 868.6 MHz for testing
}

void loop() {
  // For convenience:
  rf12_len = sizeof(rf12_data);

  // rf12_recvDone() returns 0 if nothing is received.
  if (rf12_recvDone()) {
    Serial.print("[rf12_compat] ");
    Serial.print("OK dst: ");
    Serial.print(rf12_hdr >> 24);
    Serial.print(" hdr: ");
    Serial.print(rf12_hdr & 0x00FFFFFF);
    Serial.print(" OK dst: ");
    Serial.print(rf12_dst & 0x00FFFFFF);
    Serial.println(" OK");
  } else {  // TODO: Handle error?
    Serial.print("[rf12_compat] ");
    Serial.print("Err dst: ");
    Serial.println(" Err");
  }
}
```

Here is some sample output, receiving packets from a Micro Power Snitch:

```
[rf12compat] OK dst: 00 hdr: 00 IBEECFEFOG
OK dst: 00 hdr: 00 IBEECFEFOE
OK dst: 00 hdr: 00 IBEECFEFOG
OK dst: 00 hdr: 00 IBEECFEFOE
OK dst: 00 hdr: 00 IBEECFEFOE
OK dst: 00 hdr: 00 IBEECFEFOE
```

The destination is 00FF or 000F => 0, i.e. this is a broadcast.

The sending node ID is 00FF or 000F => 63, i.e. the sender’s node ID is 63.

The rest is the 5-byte payload (4 h/w ID bytes, and type 1 = MPS with 2-bit sequence).

Some code refinements are still needed — the exact settings have not yet been optimised for both modules to inter-operate as well as possible. This results in some packets still being missed (IOW, the CRC not matching up). Also, as usual with the RF12 driver, there are occasional noise packets coming in (again with non-matching CRC, and easily flagged). But as you can see, the basic mechanism is working: RFM128-based nodes can successfully participate in a network designed for RFM69’s, all running in native packet mode!

In summary, the RF12 driver in JeeLib can now be used in three different ways:

- as, in the “traditional” mode: classic + RFM12 + RF12 = Arduino + AVR
- in RF69_COMPAT mode: classic + RFM69 + RF12 = Arduino + AVR
- in RF12_COMPAT mode: native + RFM12 + RF12 = Arduino + AVR

The two compatibility modes are a compromise compared with all-RFM12 or all-RFM69 network, simply because these modules are being pushed in some very unusual ways using various software tricks, but these modes should nevertheless come in handy for existing networks, where you don’t have the option to choose 100% uniform hardware.

[Back to article]
### JeeLib "Classic" Packet

<table>
<thead>
<tr>
<th>Preamble</th>
<th>SYN</th>
<th>Header</th>
<th>Payload (n)</th>
<th>CRC</th>
</tr>
</thead>
<tbody>
<tr>
<td>0xAA</td>
<td>0xAA</td>
<td>0xAA</td>
<td>0x2D</td>
<td>Grp</td>
</tr>
</tbody>
</table>

Hdr byte: Bits 7 - 5: flags, bits 4 - 0: source. This CRC is not the same as the RFM69 CRC.

### Hope RFM69 Variable Length Packet

<table>
<thead>
<tr>
<th>Preamble</th>
<th>SYN</th>
<th>Header</th>
<th>Payload (n)</th>
<th>CRC</th>
</tr>
</thead>
<tbody>
<tr>
<td>0xAA</td>
<td>0xAA</td>
<td>0xAA</td>
<td>0x2D</td>
<td>Grp</td>
</tr>
</tbody>
</table>

### JeeLib "RFM69 Native" Packet

<table>
<thead>
<tr>
<th>Preamble</th>
<th>SYN</th>
<th>Header</th>
<th>Payload (n)</th>
<th>CRC</th>
</tr>
</thead>
<tbody>
<tr>
<td>0xAA</td>
<td>0xAA</td>
<td>0xAA</td>
<td>0x2D</td>
<td>Grp</td>
</tr>
</tbody>
</table>

Hdr byte: Bit 7: flag, bit 6: ack, bits 5 - 0: destination.  
Src byte: Bits 7 & 6: flags, bits 5 - 0: source.

### LowPowerLab RFM69 Packet

<table>
<thead>
<tr>
<th>Preamble</th>
<th>SYN</th>
<th>Header</th>
<th>Payload (n)</th>
<th>CRC</th>
</tr>
</thead>
<tbody>
<tr>
<td>0xAA</td>
<td>0xAA</td>
<td>0xAA</td>
<td>0x2D</td>
<td>NetId</td>
</tr>
</tbody>
</table>